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Art Unit: 2817

From: Wendell E. Miller, Agent for Applicants, Reg. No. 26,572

Re: Application No. 10/644,115 - Confirmation No. 6515

Filed: August 20, 2003

Inventors: Barry A. Lautzenhiser et al.

For: SHARED-CURRENT ELECTRONIC SYSTEMS

Docket No: 203-12CIP

Enclosed: TRANSMITTAL FORM, PTO/SB/21, 1 page, 1 copy.
COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE, 5 pages, 1 copy.

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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/644,115 Conf. No. 6515	
	Filing Date	August 20, 2004	
	First Named Inventor	Barry A. Lautzenhiser	
	Art Unit	2817	
	Examiner Name	Henry Choe	
Total Number of Pages in This Submission	6	Attorney Docket Number	203-12CIP

ENCLOSURES (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
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Signature	<i>Wendell E. Miller</i>	
Date	September 14, 2004	

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/644,115

Confirmation No. 6515

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For: SHARED-CURRENT ELECTRONIC SYSTEMS

SEP 14 2004

Applicants: Barry A. Lautzenhiser et al.

Filed: August 20, 2003

TC/A.U.: 2817

Examiner: Henry Choe

Docket No.: 203-12CIP

MS Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Sir:

Applicants herein submit comments, in accordance with 37 CFR 1.104, on Examiner's "Reasons for Allowance" which were included with a Notice of Allowance of August 17, 2004. In the Notice of Allowance, Examiner allowed

5 independent Claims 1, 2, 5-8, 11, and 12, and dependent Claims 13-18, 21, and 22.

Examiner said, "Regarding claims 1, 2, 7, and 8, the closest prior art of record, Vendelin et al (Fig. 2) does not disclose the following limitations: means

for rf decoupling further includes means for making an effective series

10 resistance between the lower dc voltage terminal of the one electronic device

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and the electrical ground [and] lower than a porcelain capacitor which resonates at an rf frequency of the one electronic device" (redundant word bracketed by Applicants).

Applicants respectfully submit that Examiner has judged correctly with regard to Vendelin et al. and Applicants' independent Claims 1, 2, 7, and 8. That is, Vendelin et al. do not even mention any problem with rf decoupling, and do not even mention that a capacitor can have an effective series resistance. Therefore, independent Claims 1, 2, 7, and 8 are, indeed, allowable for the reason that Examiner stated.

However, Applicants respectfully submit that independent Claims 2, 7, and 8 are also allowable for a different reason; so that Claims 2, 7, and 8 would be allowable without the rf decoupling recitation.

With regard to independent Claim 2, Applicants respectfully submit that patentability alternately/additionally resides in the recitation, "means . . . for proportioning first and second percentages of a dc source voltage . . . to said first and second electronic devices, respectively."

More particularly, the cited art, and all of the art known to Applicants, divides the source voltage equally, whereas the source voltage is divided in selected proportions in the present invention. The means for selectively proportioning is VD1 of Figures 1-5 and 16-21, 54 of Figures 6 and 7, and 72 of Figures 8 and 10.

With regard to independent Claim 7, Applicants' arguments for independent Claim 2 also pertain to independent Claim 7. However, in independent Claim 7, the patentable recitation is: "d) proportioning first and second percentages of said dc source voltage to separate ones of said electronic devices."

With regard to independent Claim 8, Applicants' arguments for independent Claim 2 also pertain to independent Claim 8. However, in independent Claim 8, the patentable recitation is "c) proportioning first and second percentages of said dc source voltage between said first and second electronic devices."

In his Reasons for Allowance, Examiner says, "Regarding claims 5, 6, 11, and 12, the closest prior art of record, Vendelin et al (Fig. 2) does not

disclose the following limitations: means for delivering an rf signal from the baseband processor to the solid state electronic device."

With regard to independent Claim 5, Applicants respectfully submit that this claim recites, "means . . . for delivering an rf signal from said baseband
5 processor to said solid-state electronic device" (3rd recitation), as Examiner says.

Applicants respectfully submit that independent Claim 5 is alternately/ additionally allowable in that Claim 5 recites a baseband processor (110) of Figures 19-21) in dc series with a solid-state device (Q1, Q2, or Q3), as recited in the third recitation.

10 Applicants respectfully submit that, neither the cited art, nor any art known to Applicants, connects a processing electronic device (such as 92 of Figures 16-18 or 110 of Figures 19-21) in dc series with a solid-state device (Q1, Q2, or Q3). Therefore Applicants respectfully submit that independent Claim 5 would be allowable without the rf decoupling recitation.

15 Applicants respectfully submit that independent Claim 5 is also alternately/additionally allowable in that the baseband processing device and the solid-state electronic device are connected in both rf series and dc series, as recited in the third and fourth recitations.

Finally, Applicants respectfully submit that independent Claim 5 is also
20 allowable by virtue of still a different recitation, in accordance with the arguments submitted for allowability of Claim 2. That is, in Claim 5, the penultimate recitation reads: "means . . . for proportioning first and second percentages of a dc supply voltage . . . to respective ones of said devices."

With regard to independent Claim 6, Applicants respectfully submit that
25 the multiplier/upconverter (112) is connected in rf series with the baseband processor (2nd recitation), and the multiplier/upconverter is connected in rf series to the solid-state amplifying device (4th recitation).

Applicants respectfully submit that independent Claim 6 is alternately/additionally allowable by virtue of the fifth recitation in which the
30 solid-state amplifying device and the baseband processing device are connected in dc series.

Applicants respectfully submit that independent Claim 6 is alternately/ additionally allowable by virtue of the second and fourth recitations in which

three electronic devices are connected in rf series and the fifth recitation in which the solid-state amplifying device and the baseband processing device are connected in dc series.

Further, Applicants respectfully submit that independent Claim 6 is
5 alternately/additionally allowable by virtue of the second and fourth recitations in which a processing electronic device (92) and two other electronic devices are connected in rf series, and the fifth recitation in which two of the electronic devices are connected in dc series.

Finally, Applicants respectfully submit that independent Claim 6 is
10 alternately/additionally allowable by virtue of the penultimate recitation in which proportioning of the source voltage, between the baseband processor and the solid-state electronic device, is recited.

With regard to independent Claim 11, Applicants respectfully submit
that the rf series connection referred to by Examiner is: "a) rf connecting a
15 baseband processing device and a solid-state electronic device." While this recitation does not include the word, "series," those knowledgeable in the art will recognize that rf connection of these two devices must be in series.

Applicants respectfully submit that independent Claim 11 is alternately/
additionally allowable by virtue of recitation "b" in which the solid-state
20 electronic device and the baseband processing device are connected in dc series.

Again with regard to independent Claim 11, Applicants respectfully
submit that this claim is alternately/additionally allowable by virtue of recitation
"e" which reads, "separately proportioning first and second percentages of said
25 dc source voltage to said devices."

With regard to independent Claim 12, Applicants respectfully submit
that the rf series connection referred to by Examiner can be found in either the
recitation of step "a" or step "b" or in the combination of steps "a" and "b."

Applicants respectfully submit that independent Claim 12 is alternately/
30 additionally allowable by virtue of recitation "c" in which the solid-state amplifying device and the baseband processing device are connected in dc series.

Again with regard to independent Claim 12, Applicants respectfully submit that this claim is alternately/additionally allowable by virtue of recitation "f" which reads, "proportioning first and second percentages of said dc source voltage to separate ones of said devices."

5 In summary, Applicants respectfully submit that independent Claims 1, 2, 5, 6, 7, 8, 11, and 12 are indeed allowable in accordance with the reasons that have been set forth above.

10 With regard to dependent Claims 13-18, 21, and 22, Applicants respectfully submit that these claims are allowable both by virtue of dependency upon an allowable claim and their individual recitations in combination with their respective parent claims.

15 Finally, Applicants respectfully submit that independent Claims 2, 5, 6, 7, 8, 11, and 12 would have been allowable if any one, or any combination, of the patentable recitations, as discussed above, had been included in any of these independent claims.

Respectfully submitted,



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September 14, 2004

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